1. Answer the following questions concisely. Use the semiconductor parameters in the following Table if needed.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Electron diffusion coefficient</td>
<td>( D_n )</td>
</tr>
<tr>
<td>Hole diffusion coefficient</td>
<td>( D_p )</td>
</tr>
<tr>
<td>Electron mobility</td>
<td>( \mu_n )</td>
</tr>
<tr>
<td>Hole diffusion coefficient</td>
<td>( \mu_p )</td>
</tr>
<tr>
<td>Electron diffusion length</td>
<td>( L_n )</td>
</tr>
<tr>
<td>Hole diffusion length</td>
<td>( L_p )</td>
</tr>
</tbody>
</table>

a. Two NPN BJTs have identical doping levels. The base width of BJT-A is half that of BJT-B. Under the same base-emitter bias voltage, which BJT has higher collector current?

b. What is the ratio of the collector currents in NPN and PNP BJTs if they have identical dimensions and doping concentrations (but opposite doping types)?

c. What is the ratio of the transconductances \( g_m \) of NMOS and PMOS if they have identical dimensions, doping concentrations, magnitudes of threshold voltages and gate-to-source bias voltages?

(a) \( I_c = I_S e^{V_{BE}/V_T} \), \( I_S = \frac{B A e_R n_C^2}{N_A W} \times \frac{1}{W} \)

So BJT-A will have 2x higher collector current.

(b) \( \frac{I_c(NPN)}{I_c(PNP)} = \frac{D_n}{D_p} = \frac{30}{10} = 3 \)

(c) \( I_D(NMOS) = \frac{k_n}{2} (V_{GS} - V_n)^2 \), \( g_m(NMOS) = k_n (V_{GS} - V_n) \)

\( I_D(PMOS) = \frac{k_p}{2} (|V_{GS}| - |V_p|)^2 \), \( g_m(PMOS) = k_p (|V_{GS}| - |V_p|) \)

\( \frac{g_m(NMOS)}{g_m(PMOS)} = \frac{k_n}{k_p} = \frac{\mu_n C_{ox}}{\mu_n C_{ox}} = \frac{1200}{400} = 3 \)
2. Consider the two amplifiers shown below (only the AC circuit is shown). The NPN BJT has a current gain of 100, and a $v_{CE, sat} = 0.3V$, and the NMOS has $k_n = 1\, mA/V^2$ and $V_{tn} = 1V$. Ignore Early effects. Assume both transistors are biased at 0.5 mA.
   a. Find the voltage gains of both amplifiers. Which amplifier has higher gain?
   b. Which amplifier (BJT or NMOS) has higher input resistance? What's its value?
   c. What are the output resistances of both amplifiers?
   d. Which amplifier (BJT or NMOS) has large output swing? What's its value? (Note: output swing is defined as the smaller of the upward and downward voltage swings)

   \[ I_C = I_D = 0.5\, mA \]

   **BJT**
   \[ g_m = \frac{I_C}{V_T} = 40 \times 0.5 = 20\, mS \]

   **MOS**
   \[ I_D = \frac{1}{2} k_n V_{oV}^2 = 0.5mA, \quad R_u = 1mA/V^2 \]
   \[ \Rightarrow V_{oV} = 1V \]
   \[ g_m = R_u V_{oV} = 1\, mS \]

   (a) \[ A_v (BJT) = -g_m R_C = -100 \, V/V \]
   \[ A_v (MOS) = -g_m R_D = -5 \, V/V \]

   (b) \[ R_{in} (BJT) = R_{in} = \frac{B}{g_m} = 100 \times 50 = 5K\Omega \]
   \[ R_{in} (MOS) = \infty \Rightarrow \text{larger} \]

   (c) \[ R_0 (BJT) = R_C = 5K\Omega \]
   \[ R_0 (MOS) = R_D = 5K\Omega \]

   (d) **BJT**
   \[ V_C = 5 - I_C R_C = 2.5V \]
   \[ \text{Upward swing} \quad V_C^+ = V_{CC} - V_C = 2.5V \]
   \[ \text{downward} \quad V_C^- = V_C - V_{CC, sat} = 2.5 - 0.3 = 2.2V \]
   \[ \text{Output swing} = \pm 2.2V \]
(Continuation of Problem 2 solution)

MOS: \[ V_D = V_{DD} - I_D R_D = 2.5V \]

Upward swing: \[ V(+) = V_{DD} - V_D = 2.5V \]

Downward swing: \[ V(-) = V_D - V_{D, sat} = 2.5 - V_{0V} = 1.5V \]

Overall swing: \[ \pm 1.5V \]

\[ \Rightarrow \text{BJT Amplifier has larger output swing} \]
3. The NMOS below has $k_n = 1 \text{ mA/V}^2$, $V_{tn} = 1\text{V}$, and $V_A = 100\text{V}$.
   a. What is the amplifier configuration?
   b. Find the DC bias current and drain-source voltage. You can assume $V_A = \infty$ for DC analysis.
   c. Find the small signal parameters, $g_m$, $r_o$.
   d. What is the overall gain of the amplifier including the signal resistance and load resistance?
   e. What is the maximum output voltage swing? (Note: output swing is defined as the smaller of the upward and downward voltage swings)

(a) Common Source Amplifier

(b) $I_D = \frac{1}{2} k_n (V_{GS} - V_{TN})^2 = \frac{V_S}{R_2}$

$V_{GS} = V_G - V_S$
$V_G = V_{DD} \cdot \frac{R_4}{R_3 + R_4} = 5\text{V}$

$\Rightarrow \frac{1}{2} \cdot 1 \cdot (4 - V_S)^2 = \frac{V_S}{2} \Rightarrow V_S^2 - 8V_S + 16 = V_S$

$\Rightarrow V_S = 2.44\text{ V}$ (the other solution of 6.6 V leads to cut-off of MOS)

$I_D = 1.22\text{ mA}$, $V_D = 10 - I_D \cdot 5\text{k}\Omega = 3.9\text{V}$, $V_{DS} = 1.46\text{V}$

(c) $g_m = \frac{2 I_D}{V_{DS}} = \frac{2.44}{(5 - 2.44 - 1)} = 1.56\text{ mS}$

$R_D = \left(\frac{V_D}{I_D}\right)^{-1} = 82\text{k}\Omega$

(d) $G_V = -\frac{R_{in}}{R_{in} + R_{in}} \cdot \frac{A_{vo}}{R_D + R_L} = -0.98 \left(-g_m\right)(R_1//R_6//R_D) \frac{4.5\text{k}\Omega}{4.5\text{k}\Omega}$

$R_{in} = R_3//R_4 = 5\text{ M}\Omega$

$G_V = -6.87\text{ V/V}$
(Continuation of Problem 3 solution)

(e) \( V_D = 3.9 \text{ V} \)

\[ \text{Upward swing} = V_{DD} - V_D = 10 - 3.9 = 6.1 \text{ V} \]
\[ \text{Downward swing} = 3.9 - 4 = -0.1 \]
\[ V_G - V_en = 4 \text{ V} \]

\[ \Rightarrow \text{No swing} \]

(The circuit is not well designed!)

(The MOSFET is not in saturation, \( V_{DS} < V_{OV} \))
3. The NMOS below has \( k_n = 1 \text{mA/V}^2 \), \( V_{tn} = 1 \text{V} \), and \( V_A = 100 \text{V} \).
   
a. What is the amplifier configuration?
   
b. Find the DC bias current and drain-source voltage. You can assume \( V_A = \infty \) for DC analysis.
   
c. Find the small signal parameters, \( g_m, r_o \).
   
d. What is the overall gain of the amplifier including the signal resistance and load resistance?
   
e. What is the maximum output voltage swing? (Note: output swing is defined as the smaller of the upward and downward voltage swings)

   \((a)\) **Common Source**

   \((b)\) \( I_D = \frac{1}{2} k_n (V_{GS} - V_t) \), \( V_{GS} = V_G - V_S = V_{DD} \frac{R_4}{R_3 + R_4} - V_S \)

   \[ \Rightarrow \frac{1}{2} \frac{1}{2} (5 - V_S - 1)^2 = \frac{V_S}{2} \]

   \[ \Rightarrow (4 - V_S)^2 = 2V_S \Rightarrow V_S^2 = 8V_S + 16 = 2V_S \]

   \[ V_S = 2 \text{V} \text{ (8V is unphysical)} \]

   \[ I_D = 1 \text{mA} \]

   \[ V_{DS} = V_{DD} - I_D (R_1 + R_2) = 3 \text{V} > V_{ov} = V_{GS} - V_{tn} = (5 - 2) - 1 \Rightarrow 2 \text{V} \]

   \((c)\) \( g_m = \frac{2I_D}{V_{ov}} = \frac{2}{2} = 1 \text{mS} \)

   \[ R_0 = \frac{V_A}{I_D} = 100 \text{K} \Omega \]

   \((d)\) \( G_V = -\frac{R_{in}}{R_{se}} (g_m)(R_1 || R_0 || R_6) = -4.4 \text{ V/V} \)

   \[ R_{in} = R_3 || R_4 = 5 \text{M} \Omega \]

   \((e)\) \( V_C(t) = V_{DD} - V_D = 5 \text{V} \)

   \[ V_C(-) = V_D - V_{ov} = 5 - 2 = 3 \text{V} \]

   \[ \Rightarrow \text{Output swing} = \pm 3 \text{V}. \]
4. The NPN BJT has a current gain of 100. Assume Early voltage $V_A = \infty$.
   a. What is the amplifier configuration?
   b. What is the input resistance (not including $R_L$)?
   c. What is the output resistance, $R_{out}$ (including $R_{sig}$)?
   d. What is the overall voltage gain, including the signal and load resistance?
   e. What is the overall current gain of the amplifier, i.e., the ratio of the current flowing $R_L$ and current from source?

\[ G_{v} = \frac{R_{in}}{R_{s}+R_{in}} \cdot g_m \cdot (R_{1}+R_{2}) \]
\[ = \frac{26}{50+26} \cdot 0.04 \cdot (2.5k) \]
\[ G_{v} = 34 \, \text{V/V} \]

\[ G_{i} = \frac{R_{s}g_{m}}{R_{in}+R_{s}g_{m}} \cdot A_{i} \]
\[ A_{i} = \frac{i_{o}}{i_{in}} \]

\[ i_{o} = -\beta i_{b} \left( \frac{R_{1}}{R_{L}+R_{1}} \right) \]
\[ i_{in} = -(\beta+1) i_{b} \]
\[ i_{o} = i_{in} \left( \frac{\beta+1}{\beta} \right) \left( \frac{R_{1}}{R_{L}+R_{1}} \right) \]

\[ A_{i} = \frac{i_{o}}{i_{in}} = \frac{R_{1}}{R_{L}+R_{1}} = \frac{5k}{5k+5k} = 0.5 \, \text{A/A} \]

Full credit was given for this answer.

The question asked for overall current gain need to consider source resistance.

\[ G_{i} = \frac{R_{s}g_{m}}{R_{in}+R_{s}g_{m}} \]
\[ A_{i} = \frac{50}{50+26} \cdot 0.5 = 0.33 \, \text{A/A} \]
5. The PNP BJT has a current gain of 100. Ignore Early effect.
   a. What is the amplifier configuration?
   b. Find the DC bias point (collector current and collector-to-emitter voltage).
   c. What is the input resistance, $R_{in}$ (not including $R_L$)?
   d. What is the output resistance, $R_{out}$ (including the effect of $R_{sig}$)?
   e. What is the overall voltage gain including signal and load resistances?

\[ \text{Common collector (emitter follower)} \]

\[ V_{EQ} = 0 \]
\[ R_B = 200 \, k\Omega / 200 \, k\Omega = 100 \, k\Omega \]
\[ 5V - I_E R_1 - V_{EB} - I_B R_O = 0 \]
\[ 5V - I_E R_1 - V_{EB} - \frac{I_C}{10} R_O = 0 \]
\[ I_C = 1 \, mA \]
\[ V_{CE} = -(5 - 3.3 + 5V) \]
\[ V_{CE} = -6.7 \, V \]

\[ R_{in} = \frac{(\beta + 1)(r_e + R_1)}{R_O} \]
\[ = (101)(26 + 300) / 100 \, k\Omega \]
\[ R_{in} = 77 \, k\Omega \]

\[ R_{in} \text{ (including } R_L) = \frac{(\beta + 1)(r_e + R_1 / R_L)}{R_O} \]
\[ = (101)(26 + 97) / 100 \, k\Omega \]
\[ = 11 \, k\Omega \]

\[ 6V = \frac{R_{in}}{R_{sig} + R_{in}} \frac{R_L / R_1}{R_L / R_1 + r_e} \]
\[ = \left( \frac{11 \, k\Omega}{11 \, k\Omega + 10 \, k\Omega} \right) \left( \frac{97}{97 + 25} \right) \]
\[ = 0.42 \, V/V \]