Final Exam

EE40 - Summer 2014

Gerd Grau

Name:

Discussion section:

Discussion GSI:

Student ID:

Instructions:

Unless otherwise noted on a particular problem, you must show your work in the space provided or on the back of the exam pages.

Underline your answers to each problem with a double line.

Simplify your answers as far as possible unless otherwise noted.

Be sure to provide units where necessary.

GOOD LUCK!

Question	Points	Max
1		13
2		23
3		25
4		29
Total		90

Question 1 (13 points):



- a) Consider the below Bode phase plot. Write down the simplest transfer function i.e. with the fewest terms that will describe this Bode plot. Use the standard form discussed in lecture e.g. $(1 + j\omega/5)$. Assume the constant pre-factor is +1 and any term including j must also include ω .
- b) Draw the corresponding Bode magnitude plot.





Question 2 (23 points):

a) Consider the below op-amp circuit. Determine the AC transfer function $H(\omega) = v_o/v_{in}$. Put the final result into the standard form. All op-amps are ideal and you can use the negative feedback approximation for all of them.



$$\frac{V_{01}}{V_{in}} = 1 + \frac{Z_{c}}{Z_{R}} = 1 + \frac{1}{5^{10}c_{R}} = \frac{1 + \frac{1}{5^{10}} \times 10k \times 2m}{5^{10} \times 10k \times 2m} = \frac{1 + \frac{1}{5^{10}} \times \frac{1}{5^{10}} \times \frac{1}{5^{10}} \frac{1}{$$

2: In variing applifier

$$\frac{V_{02}}{V_{01}} := -\frac{Z_{11}}{Z_{1}} := -\frac{\frac{R}{1+R_{02}K}}{\frac{1+R_{02}K}{3}} := -\frac{ZOK}{\frac{1+R_{02}K}{3}} := -\frac{ZOK}{\frac{1+R_{02}K}{3}} := -\frac{ZOK}{\frac{1+S}{5}}$$

3: Buffer Vo3 = Voz

$$\frac{V_{0}}{V_{03}} = 1 + \frac{1/k}{1} + \frac{1}{102m} = 1 + \frac{1}{100} +$$

 $H(\omega) = \frac{V_0}{V_{11}} = \frac{V_0}{V_{12}} \cdot \frac{V_{01}}{V_{02}} \cdot \frac{V_{02}}{V_{01}} \cdot \frac{V_{01}}{V_{01}}$

K=3x1x10"x250x0.05 = 1 x 103 x 125 = 7.5 * 105

 $=\frac{3(1+j\frac{\omega}{+50}) \cdot 1 \cdot (-20k) \cdot (1+j\frac{\omega}{0.05})}{j\frac{\omega}{50} \times (1+j\frac{\omega}{5})} = -\frac{1.5 \cdot 10^{5}(1+j\frac{\omega}{+50}) \cdot (1+j\frac{\omega}{0.05})}{(j\omega)^{2}(1+j\frac{\omega}{5})}$

- b) Answer the following questions:
 - i) Write down the corner frequencies of all simple zeroes in rad/sec (write N/A if none):

150,0.05

5

NIA

- ii) Write down the corner frequencies of all simple poles in rad/sec (write N/A if none):
- iii) Write down the corner frequencies of all quadratic zeroes in rad/sec (write N/A if none):
- iv) Write down the corner frequencies of all quadratic poles in rad/sec (write N/A if none):

NIA

- v) How many zeroes at the origin are there?
- vi) How many poles at the origin are there?



vii) What will be the phase for $\omega \to \infty$

2 * (+90°) + 3* (-90°) = - 90°

viii) What will be the slope of the magnitude Bode plot for $\omega \rightarrow \infty$

20 dB (+2-3) = - 20 dB/dec



Question 3 (25 points):

Consider the below oscillator circuit. You can assume that the op-amp is ideal except for a limited supply voltage of ±5V.

- a) Calculate the oscillation period T i.e. the time for one full oscillation cycle as a function of R and C. The voltage V_1 is 0V.
- b) Now a DC voltage V_1 is applied by the battery. Calculate the new period T as a function of R, C and V_1 .
- c) For $V_1 = 4V$ draw the output waveform. Carefully label both axes in terms of V_{supply} , R and C.

If any of your answers contain any terms in the form of e^A or $\ln(A)$ where A is an integer or a fraction of two integers, you can leave it in that form.



a)
$$V_{\pm} = \frac{V_0}{2}$$
 (potential divider, ideal open p, $V_{\pm} = 0$)
 $KLL \ at \ v_{\pm}:$
 $C \frac{dv_{\pm}}{dt} + \frac{v_{\pm}-V_0}{R} = 0$
 $Case |: V_0 = \pm 5V$
 $v_{\pm} = -RC \frac{dv_{\pm}}{dt} \pm V_0$
 $\Rightarrow v_{\pm}(t) = v_{\pm}(\infty) \pm (v_{\pm}(0) - v_{\pm}(\infty))e^{-t/2}$ $T = RC$

b)
$$V_1 \neq 0 \Rightarrow V_+ = \frac{V_0 + V_1}{2}$$

For $V_0 = +5V$: $V(T_+) = \frac{V_0 + V_1}{2} = \frac{5 + V_1}{2}$
For $V_0 = -5V$: $V(T_-) = \frac{-5 + V_1}{2}$
(ase 1: $V_0 = +5V$
 $V_-(0) = \frac{-5 + V_1}{2}$
 $\frac{5 + V_1}{2} = 5 + (\frac{-5 + V_1}{2} - 5)e^{-T_1/2}$

$$\frac{V_{i}}{2} - \frac{S}{2} = \left(\frac{V_{i}}{2} - \frac{3}{2} + S\right)e^{-S_{i}/E}$$

$$\frac{F_{i}/E}{2} = \frac{V_{i} - 15}{V_{i} - 5}$$

$$T_{+} = R C hn \left(\frac{V_{i} - 15}{V_{i} - 5}\right)$$
Case 2: $V_{0} = -SV$

$$V_{-}(0) = \frac{S + V_{i}}{2}$$

$$\frac{-S + V_{i}}{2} = -S + \left(\frac{S + V_{i}}{2} - (-S)\right)e^{-T_{-}/E}$$

$$\frac{V_{i}}{2} + \frac{S}{2} = \left(\frac{3}{2} \cdot S + V_{i}\right)e^{-T_{-}/E}$$

$$\frac{V_{i}}{2} + \frac{S}{2} = \left(\frac{3}{2} \cdot S + V_{i}\right)e^{-T_{-}/E}$$

$$\frac{V_{i}}{2} + \frac{S}{2} = \left(\frac{3}{2} \cdot S + V_{i}\right)e^{-T_{-}/E}$$

$$\frac{V_{i} + 15}{V_{i} + 5}$$

$$T_{-} = R C hn \left(\frac{V_{i} + 15}{V_{i} + 5}\right)$$

$$T = T_{+} + T_{-}$$

$$= RC - \left(hn \left(\frac{V_{i} - 15}{V_{i} - 5}\right) + hn \left(\frac{V_{i} + 15}{V_{i} + 5}\right)\right)$$

C) V,=4V $T_{+} = RCh(\frac{4-15}{4-5}) = R(h(\frac{4}{7}) = 2.4 RC$ $T_{-} = R(l_{\frac{4+15}{4+5}}) = R(l_{\frac{19}{4}}) = 0.75R(l_{\frac{19}{4}}) = 0.75R(l_{\frac{19}{4}) = 0.75R(l_{\frac{19}{4}) = 0.75R(l_{\frac{19}{4})} = 0.75R(l_{\frac{19}{4}) = 0.75R(l_{\frac{19}{4}) = 0.75R(l_{\frac{19}{4}) = 0.75R(l_{\frac{19}{4}) = 0.75R(l_{\frac{19}{4}) = 0$

don't weed to calculate enact values if reclized effect on duty cycle





Question 4 (29 points):

Imagine a customer hires your company to design a circuit that will implement the below transfer function:

$$H(\omega) = -\frac{j\frac{\omega}{100} \times j\frac{\omega}{20}}{\left(1 + j\frac{\omega}{100}\right)\left(1 - \frac{\omega^2}{6}\right)}$$

Due to their current fabrication processes they constrain the types of devices you can use. You are allowed to use two MOSFET transistors and as many capacitors of any capacitance value, inductors of any inductance value and $10k\Omega$ resistors as you need. Your colleague has already started the design process and left you the below circuit drawing before he went on vacation. You need to finish the job by filling in the right impedances. Draw your combinations of R, L, C devices (including values) and simple wires into the blank boxes provided. Clearly justify each choice you make with calculations. Since each additional component translates into extra cost, you will lose points for non-optimum solutions that use a larger number of components than necessary.

You can assume that the applied DC voltages put the MOSFETs into the operating regime that allows you to use the MOSFET small signal equivalent circuit from class. The transconductance of the MOSFETs g_m is 0.1A/V. You can assume that r_d is large enough to be ignored. You can use superposition to ignore the effect of the DC sources. However, you cannot block this DC bias with your impedances i.e. don't use a single capacitor or a capacitor in a purely serial combination of components since that would act as a DC block.



Can anlyze MOSTET stages independently since Zin= 00 for ideal MOSFETS. First stage common droin : Equivalent arcust: Vin (125 Vo, 120 Vgs=Vin-Vs=Vin-Voi $V_{01} : q_{m} V_{qs} z_{s} = q_{m} z_{s} (V_{in} - V_{01})$ $\frac{V_{01}}{V_{1n}} = \frac{g_m z_s}{1 + g_m z_s}$ Independent of Zo & Zo can be simple wire (Zo=0). Same cut-off for simple pole and shifted zero at origin. Looks like ju/100 in desired H(w). (Itjw/100) ≥) gm Zs=j~/100 ≥) Zs is inductor with julg==j0/100 $L = \frac{1}{(00 \times 0.1)} = 0.1 H$ 11/13



$$V_0 = -\frac{2}{30}g - \frac{1}{9}s$$

 $V_{gs} = V_{o1} - g - \frac{1}{9}s \frac{2}{5}s$ $V_{gs} = \frac{1}{1+g-2}s$

$$\frac{V_0}{V_{01}} = \frac{Z_0 q_m}{1 + q_m r^2 s} = -\frac{S_0/20}{(1 - w_0^2)} \qquad (remaining port of Hlh)$$

$$-\omega^2 term suggests LC. Can't term series, has to be prallel:$$

$$Z_L | I_{2L} = \frac{S_0 L^{\frac{1}{2} + \omega_L}}{S_0 L + \frac{1}{S_0 L}} = \frac{S_0 L}{1 - \omega^2 L L} = \frac{2}{0} \qquad g_m r^2 D = \frac{S_0 k 0}{1 - \frac{\omega^2}{6}}$$

$$q_m g_0 L = \frac{S_0}{2} \sqrt{20} \quad \Re L = \frac{1}{20 \cdot 0 \cdot 1} = \frac{0 \cdot SH}{6 L - \frac{1}{6}}$$

2)/+g-2;=1 2) 2; -9 (wire)