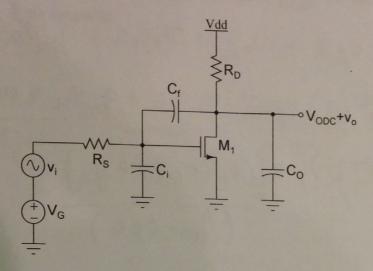
1. (60 points) Frequency Response. For the circuit shown below, assume  $M_1$  operates in saturation and has the following defining parameters: W, L,  $\mu_n$ ,  $C_{OX}$ ,  $V_{TH}$  and  $\lambda$ .



(a) (8 points) Express the small-signal parameters  $g_m$  and  $r_o$  for  $M_1$  for the DC bias condition  $V_G > V_{TH}$  in terms of the defining parameters and  $V_G$ .

$$I_{p} = \mu Con (V_{g} - V_{Th})^{2} (1 + \lambda V_{DS}) \rightarrow 0$$

$$g_{m} = \frac{2I_{D}}{V_{g} - V_{Th}} \rightarrow 0, \quad g_{0} = \frac{1}{\lambda I_{D}} \rightarrow 0$$

$$Substitude \quad for I_{D} \quad \text{ in } \quad \text{ and } \quad \text{ 3}$$

(b) (8 points) Draw the small signal model for the circuit shown above.

Transfer function 
$$RL = \frac{1}{g_L} g_S = \frac{1}{R_S} s = \frac{1}{g_S}$$

KCL at node  $V_S$ 
 $(V_O - v_n) c_p s + v_0 g_L + v_0 c_S + q_m v_n = 0$ 
 $v_0 (c_S + g_L + c_t s) - v_0 c_t s + q_m v_n = 0$ 
 $v_n = -v_0 [(c_0 + c_t) s + g_L]$ 
 $(g_m - c_t s)$ 
 $v_n = v_0 [(c_0 + c_t) s + g_L]$ 
 $(v_n - v_0) g_S + v_n c_0 s + (v_n - v_0) c_t s = 0$ 
 $v_n = v_0 [(c_0 + c_t) s + g_L] - c_t s v_0 = v_0 g_S$ 
 $v_0 = v_0 - c_t s$ 
 $v_0 = v_0 - c_t s$ 

(c) (16 points) Determine the small signal transfer function  $H(j\omega) = \frac{v_o(j\omega)}{v_i(j\omega)}$ . Use only the capacitors explicitly shown. Assume  $v_o \gg R_L$  for simplicity.

(d) (8 points) From the transfer function, identify the location of poles and zeros assuming that  $g_m R_L \gg 1$ .

docation of 300
$$\frac{9m}{3} = \frac{9m}{c_{f}}$$

$$\frac{300}{36}$$

Locating the poles Consider only denominator > [(G+Cp)s+g\_][(ci+Cp)s+gs]+cps(gm-cps) =) (co+ce)(ci+ 94)82+ [95(co+ce)+g(ci+ce)+9mce] 5+959L Converting back to Re and Rs Rs RL (GCi+CoCf+CiCf) S2+S[RL(Co+Cf)+Rg(Ci+Cf)] +Cf9mRLRs => RsRL (CoCi+CoCf+CiCf) S2 + S[RL(G+Cf)+Rs(Ci+Cf(1+gmRL))] Denominator of the form 32/p, P2 + 3[p, + 1/p] + 1 Since 9mRc >> 1 1 2 Rs (C: + Cf (1+9mRc)) Pi=

Rs (ci+cx(1+gmRi)) when cf is split

using miller

theorn. Finding P2: see last page.

(e) (8 points) We know that at low frequencies, the output is 180° out of phase with respect to input. As the input frequency increases, there is additional phase added due to the pole(s) and zero(s). Now, what is the phase of  $H(j\omega) = v_o/v_i$  at very high frequencies? (hint: The zero also does something interesting)

We have 
$$(1-S/z_1)$$
  $(S=j\omega)$ 
 $(1+S/p_1)(1+S/p_2)$ 
 $(1+S/p_1)(1+$ 

(f) (6 points) You will learn in later courses (if you still want to stay in EE @) that this kind of a zero is actually a bad thing. One way to get rid of it is with the circuit shown below. From the transfer function you obtained above, find only the new location of zero including  $R_F$  (hint: Just modify  $\frac{1}{i\omega C_F}$  to  $\frac{1}{i\omega C_F}$  +  $R_F$ )

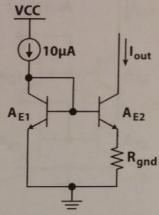
$$\frac{1}{1} \operatorname{dec} = \frac{1}{1} \operatorname{dec} + \operatorname{RF} \Rightarrow \operatorname{dec} = \frac{1}{1+\operatorname{Riwc}_{F}}$$

$$\operatorname{dec} = \frac{1}{1+\operatorname{Riw$$

To get rid of zero or push it to intinity 1-9mRF=0

$$R_{F} = \frac{1}{2m} \frac{V_{dd}}{R_{D}}$$

2. (30 points). DC bias calculations. For the circuit shown below, assume  $R_{gnd}=0$ ,  $V_A=\infty$  and  $\beta$  is large enough to neglect the base current.



(a) (6 points) Find the relation between  $A_{E1}$  and  $A_{E2}$ , so that  $I_{out} = 1 \text{ mA}$ .

$$\frac{A_{E2}}{A_{E1}} = \frac{Im A}{IO \mu A} = 100 \implies \boxed{A_{E2} = 100 A_{E1}}$$

(b) (10 points) With your sizing in Part (a), you discover that the current I<sub>out</sub> is only 90% of 1mA. After a lot of debugging, you discover that the ground connection of the mirror transistor is improper and has a resistance R<sub>gnd</sub> as shown. Find the value of R<sub>gnd</sub>.

$$Tout = 900\mu A$$

$$\beta \text{ is longe}$$

$$V_{GE1} = V_7 \ln \left( \frac{10\mu A}{T_S} \right)$$

$$V_{BE2} = V_7 \ln \left( \frac{900\mu A}{100T_S} \right)$$

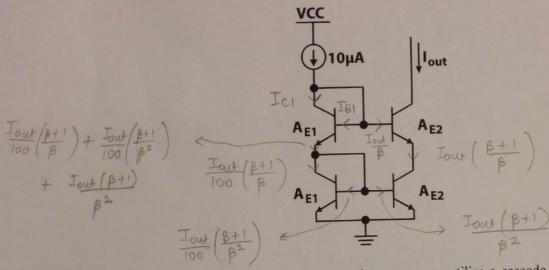
$$900\mu A \times R_{gnd} = V_{BE1} - V_{BE2}$$

$$= V_7 \ln \left( \frac{10\mu A}{T_S} \right) - V_7 \ln \left( \frac{900\mu A}{100T_S} \right)$$

$$= V_7 \ln \left( \frac{1000}{900} \right)$$

$$\Rightarrow R_{gnd} = 3.04 \Omega$$

$$5/6$$



(c) (14 points) In order to boost the output impedance, we now utilize a cascode current source. With your sizing in (a) for  $A_{E2}$ ,  $V_A = \infty$  and  $\beta = 100$ , find the value of  $I_{out}$ . Note that  $\beta$  is finite. (Hint: Express the emitter, base and collector currents of each transistor in terms of  $I_{out}$ ).

AE2 = 100 AE1 , 
$$\beta$$
 = 100

Mark the corrects

$$T_{C1} = \begin{pmatrix} \beta \\ \beta+1 \end{pmatrix} \begin{pmatrix} J_{out} \begin{pmatrix} \beta+1 \\ \beta \end{pmatrix} + \frac{101 J_{out}}{100} \begin{pmatrix} \beta+1 \\ \beta^2 \end{pmatrix} \end{pmatrix}$$

$$= \frac{J_{out}}{100} + \frac{101 J_{out}}{100\beta}$$

$$I_{B1} = \frac{J_{out}}{100\beta} + \frac{101 J_{out}}{100\beta^2}$$

$$I_{C1} + I_{B1} + \frac{J_{out}}{100\beta^2} = 10 \text{ MÅ}$$

$$\Rightarrow \frac{J_{out} \begin{pmatrix} \beta+1 \\ \beta \end{pmatrix}}{100} + \frac{101 J_{out}}{100} \begin{pmatrix} \beta+1 \\ \beta^2 \end{pmatrix} + \frac{J_{out}}{\beta} = 10 \text{ MÅ}$$

$$\Rightarrow With \beta = 100$$

$$\Rightarrow J_{out} = 330 \text{ MÅ}$$

6/6

Question 1d

$$\frac{1}{P_1 P_2} = R_L R_S (c_0 c_1 + c_0 c_4 + c_1 c_4)$$

$$\frac{1}{P_1} = R_S (c_1 + c_4 (1 + q_m R_L))$$

$$\frac{1}{P_2} = R_L R_S (c_0 c_1 + c_0 c_4 + c_1 c_4)$$

$$\frac{1}{P_1} = R_S (c_1 + c_4 (1 + q_m R_L))$$

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